



Research and Development Section
Indian Institute of Technology
Guwahati Guwahati-781039, Assam

Applications are invited for a **Walk-in interview** for the following post(s) in the project entitled, "**Nanofilled natural ester impregnated surface modified pressboards for suppressing partial discharges in transformers**" at the department of Electronics and Electrical Engineering, IIT Guwahati.

Date: 22 Feb 2024 (Thursday)

Time: 10.00 AM

Venue: EEE Department, IIT Guwahati

Sl. No.	Project Staff Designation	Number of Vacancies	Pay Recommended (Rs.)	HRA Required (Rs.)	Medical Required (Rs.)	Total Amount (Rs.)	Duration of Appointment in months	Qualifications
1	JRF (GATE)	01	31000	Yes, Rs.4960	1250.0	37210.00	06	M.E./M.Tech/M.S.(Eng.) in Electrical Engineering with GATE score specializing in Insulation and High Voltage Engineering or PhD in Electrical Engineering

								specializing in Insulation and High Voltage Engineering. The candidate should be experienced in ageing studies of solid and liquid insulation and in characterization of insulation.
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How to apply and selection process: Candidates have to appear in the Walk in Interview along with an application/CV on plain paper giving details of all educational qualifications, experience, contact address, phone no., E - mail etc. and submit photocopies of relevant documents at the **time of interview on 22 Feb 2024 (Thursday) at 10 AM. Venue: EEE Department, IIT Guwahati.**

Selection will be based on the performance of the candidate in the interview. Candidates will not be sent any call letter separately. Advance copy of CV may be sent to the Principal Investigator.

For any clarification, **contact:** Prof. Sisir Kumar Nayak (Principal Investigator)

Email: sknayak@iitg.ac.in

Phone: 0361-2582530

No campus accommodation will be available for the selected candidates. No TA/DA will be paid to the candidates for appearing in the test and interview.